	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	3229071	circuit with5 breaker	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 12:16
2	BRS	L2	152709	l1 and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 12:31
3	BRS	L3	45851	circuit adj breaker	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 12:32
4	BRS	L4	1784	l3 and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 12:39
5	BRS	L5	6	l4 and test adj generator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 13:00

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	117	l4 and (self adj test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 13:46
7	BRS	L7	3447	(test with3 generator) same (circuit adj breaker)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 13:01
8	BRS	L8	731	l7 and incorporated	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 13:02
9	BRS	L9	140	l8 and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 13:02
10	BRS	L10	35	breaker with (self adj test$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 14:06

	Type	L #	Hits	Search Text	DBs	Time Stamp
11	BRS	L11	0	breaker with (trip adj circutry)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 14:07
12	BRS	L12	30	breaker with (trip adj circuitry)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 14:09
13	BRS	L13	1	l12 and (self adj test$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/26 14:10